

01/04/2002

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(FILE 'HOME' ENTERED AT 14:12:35 ON 01 OCT 2003)

FILE 'CAPLUS, MEDLINE, USPATFULL, BIOSIS, EMBASE, SCISEARCH' ENTERED AT 14:13:02 ON 01 OCT 2003

L1 2 S (MICROARRAYS ARE MANUFACTURED ON A LARGE, SUBSTRATE PLATE OR
WAFER, WHICH IS SCORED
L2 4 S (MULTIPLE ARRAYS ARE PRODUCED CONCURRENTLY IT IS CARRIED
OUT IN A TWO DIMENSIONAL)
L3 1 S (ARE SELECTED AS ALIGNMENT SHOTS FROM MULTIPLE SHOT AREAS ON THE
WAFER)
L4 19 S COMBIMATRIX
L5 29 S DNA (P) MULTIPLE (P) ARRAY (P) WAFER
L6 25 DUP REM L5 (4 DUPLICATES REMOVED)
L7 18539 S ELECTRODE (3A) ARRAY
L8 0 S L7 (P) MULTIPLE (P) (DEVICE OR CHIP) (P) SAME (P) WAFER (P)
(SILICON OR SI)
L9 9 S L7 (P) MULTIPLE (P) WAFER (P) (SILICON OR SI)
L10 9 DUP REM L9 (0 DUPLICATES REMOVED)
L11 0 S L7 (P) MULTIPLE (P) (DEVICE OR CHIP) (P) SAME (P) WAFER
L12 9 S L7 (P) MULTIPLE (P) (DEVICE OR CHIP) (P) WAFER
L13 6 S L12 NOT L10
L14 6 DUP REM L13 (0 DUPLICATES REMOVED)
L15 54 S L7 (P) (DICE OR DICING OR DICED) (P) WAFER
L16 53 DUP REM L15 (1 DUPLICATE REMOVED)
L17 794 S L7 AND (CHEMICAL (P) (SENSOR OR SYNTHES?))
L18 1 S L17 AND (MULTIPLE (P) (DEVICE OR CHIP) (P) SAME (P) WAFER)
L19 6 S L17 AND (MULTIPLE (P) SAME (P) WAFER)
L20 5 S L19 NOT L18
L21 5 DUP REM L20 (0 DUPLICATES REMOVED)
L22 475 S SENSOR (P) (DICE OR DICING OR DICED) (P) WAFER
L23 189 S SENSOR (P) (DICE OR DICING OR DICED) (P) WAFER (P) INDIVIDUAL
L24 95 S (INDIVIDUAL (3A) SENSOR) (P) ((DICE OR DICING OR DICED) (P) WAFER)
L25 15 S L7 AND L24
L26 15 DUP REM L25 (0 DUPLICATES REMOVED)
L27 3 S MULTIPLE (P) ELECTRODE (P) ARRAYS (P) DICED (P) WAFER
L28 2 S MULTIPLE (P) ELECTRODE (P) ARRAYS (P) DICING (P) WAFER
L29 18539 S ELECTRODE (3A) ARRAY
L30 24 S L29 (P) (CHEMICAL OR BIOCHEMICAL) (P) SYNTHESIS
L31 24 DUP REM L30 (0 DUPLICATES REMOVED)
L32 2 S L31 AND (DICE OR DICED OR DICING)

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(FILE 'HOME' ENTERED AT 09:03:40 ON 01 OCT 2003)

FILE 'CAPLUS, SCISEARCH, MEDLINE, EMBASE, BIOSIS, USPATFULL' ENTERED AT
09:04:15 ON 01 OCT 2003

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L1      156 S ELECTROCHEM? (P) OLIGONUC? (P) (ARRAY OR CHIP)
L2      130 S L1 AND ELECTRODE
L3      51 S L1 AND (ELECTRODE (3A) ARRAY)
L4      46 DUP REM L3 (5 DUPLICATES REMOVED)
L5      10 S L4 AND (SILICON WAFER)
L6      52 S L2 AND (DICE OR DICING OR SEVER?)
L7      31 S L3 AND (DICE OR DICING OR SEVER? OR DICED)
L8      4 S L3 AND (DICE OR DICING OR SEVER OR SEVERING OR SEVERED OR DICED)
L9      4 DUP REM L8 (0 DUPLICATES REMOVED)
L10     4 S L2 AND (DICE OR DICING OR SEVER OR SEVERING OR SEVERED OR DICED)
L11     0 S L10 NOT L8
L12     4 S L1 AND (DICE OR DICING OR SEVER OR SEVERING OR SEVERED OR DICED)
L13     0 S L12 NOT L8
L14     26 S (MULTIPLE (3A) ARRAY) AND L1
L15     24 S (MULTIPLE (3A) ARRAY) AND L2
L16     24 DUP REM L15 (0 DUPLICATES REMOVED)
L17     0 S L16 AND (MULTIPLE (3A) ARRAY (3A) SAME (3A) WAFER)
L18     0 S L1 AND (MULTIPLE (3A) ARRAY (3A) SAME (3A) WAFER)
L19     0 S OLIGONUC? (P) (MULTIPLE (3A) ARRAY (3A) SAME (3A) WAFER)
L20     0 S (MULTIPLE (3A) ARRAY (3A) SAME (3A) WAFER)
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